

Typical Features

- ◆ Wide input voltage range (2:1)
- ◆ Typical transfer efficiency 87%
- ◆ Switching frequency: 300KHz
- ◆ Over current/Short circuit protection, Self-recovery
- ◆ Input-output isolated
- ◆ PCB Mounting
- ◆ Metal case, Low output ripple



Technique Parameters **Test Condition:** Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and $T_a=25^{\circ}\text{C}$.

Input Specifications	Min(v)	Nom(v)	Max(v)	Notes
Input Voltage Vdc	9	12	18	2:1
	18	24	36	2:1
	36	48	72	2:1
	72	110	144	2:1
Remote Control Terminal (low level)	ON	Connected to high level or suspended		3.5Vdc ~ +Vin
	OFF	Connected to Low level or ground switched off		$\leq 0.3\text{Vdc}$
Input Under-Voltage Protection	Lower than the low-end of input voltage, module switched off output, self-recovery			

Output Specifications

Output Voltage Accuracy		Vo1	$\pm 1.0\%$ (typ.)
Line regulation	Nominal Load, full voltage range	Vo1	$\pm 0.2\%$
Load regulation	20% ~ 100% nominal load	Vo1	$\pm 0.5\%$
Ripple and Noise	20MHz BM Full Load $V_o \leq 5.0\text{V}$, $\leq 50\text{mVp-p}$; $V_o \geq 48\text{V}$, $\leq 180\text{mVp-p}$; Other, $\leq 100\text{mVp-p}$		
Dynamic response	25% Nominal load step change	$\Delta V_o / \Delta t$	$\pm 4.0/500\mu\text{s}$
Output Voltage adjustment	Nominal output voltage	TRIM	$\pm 10\%$ Adjustable
Start up delay time	Typical value		$\leq 200\text{ms}$

General Specifications

Switching Frequency		300KHz (Typical)	330KHz (Max.)
Operating Temperature	Free Air Convection		$-25^{\circ}\text{C} \sim +55^{\circ}\text{C}$
Storage Temperature			$-40^{\circ}\text{C} \sim +105^{\circ}\text{C}$

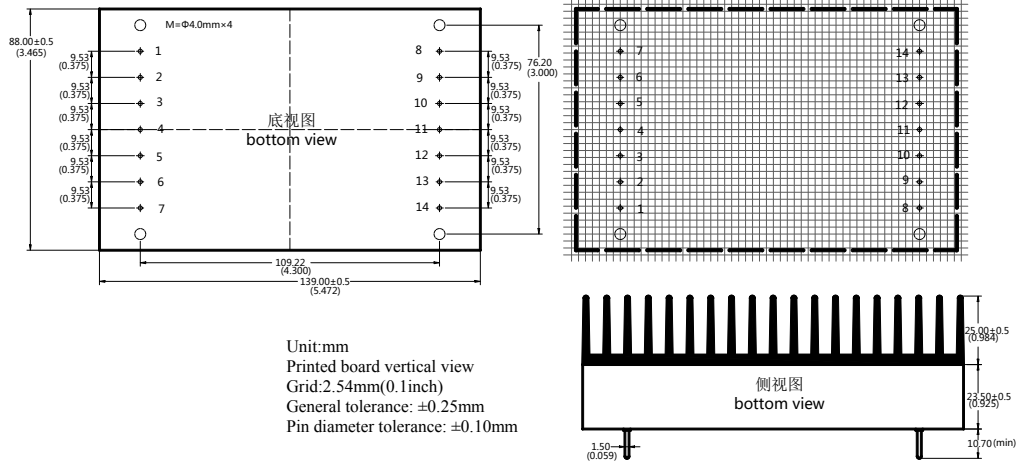
Max Case Temperature			+100℃
Relative Humidity			10%~90%
Case Material	Meta case		
Isolation Voltage	Input-output 1500 Vdc \leq 0.5mA/1min; Input-case 500Vdc \leq 0.5mA / 1min		
MTBF	2X10 ⁵ Hrs		

Typical Product List

Part No.	Input voltage range	Output voltage/ current		Input Current Nominal Voltage (typ.)	Max Capacitive Load	Efficiency (typ.)	
		Voltage(Vdc)	Current(mA)	Full load(mA)	μ F	%	
WD200-12S12P1	12 V (9~18V)	12	16666	19157	4000	87	
WD200-24S12P1	24V (18~36V)	12	16666	9470	4000	88	
WD200-24S24P1		24	8333	9470	1200	88	
WD300-24S12P1		12	25000	14205	4000	88	
WD300-24S24P1		24	12500	14045	1200	89	
WD400-24S12P1		12	33333	18939	4000	88	
WD400-24S24P1		24	16666	18727	1200	89	
WD200-48S12P1		48V (36~72V)	12	16666	4735	4000	87
WD200-48S24P1			24	8333	4735	1200	88
WD300-48S12P1	12		25000	7102	4000	88	
WD300-48S24P1	24		12500	7022	1200	89	
WD400-48S12P1	12		33333	9470	4000	88	
WD400-48S24P1	24		16666	9363	1200	89	
WD200-110S12P1	110V (72~144V)		12	16666	2066	4000	88
WD200-110S24P1			24	8333	2066	1200	88
WD300-110S12P1		12	25000	3099	4000	88	
WD300-110S24P1		24	12500	3064	1200	89	
WD400-110S12P1		12	33333	4132	4000	88	
WD400-110S24P1		24	16666	4086	1200	89	

Note: Due to space limitation, above is only a part of our product list, please contact our sales team for more items.

Packing Dimension



Pin Definition

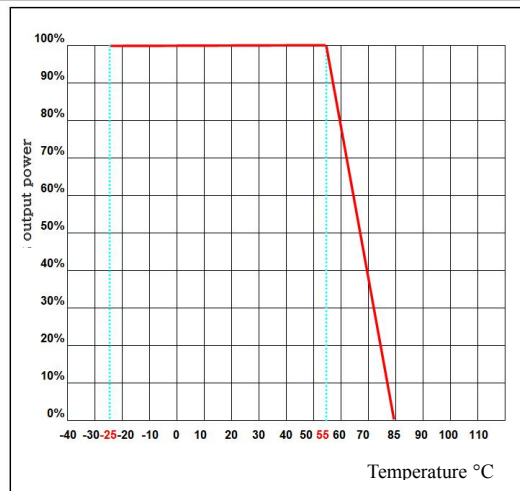
Single	1	2:3	4:5	6	7	8:9	10:11	12	13	14
(S)	NP	-Vin	+Vin	REM	CASE	GND	+Vout	-S	TRIM	+S

* Note: If the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

Dimension

Packing Code	L x W x H	
P1	139.00 × 88.00 × 23.50mm	5.472 × 3.465 × 0.925inch

Temperature Curve



Ripple & Noise Test: (Twisted Pair Method 20MHZ bandwidth)

a, 12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 47uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b, Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.

